

Appl. No. 09/821,636
Amdt. Dated August 12, 2005
Reply to Office Action of March 28, 2005

REMARKS

Claims 40 and 41 have been added in order to claim further subject matter as disclosed in the specification.

Applicants respectfully request reconsideration of claim 2 under 35 U.S.C. §112. Support for claim 2 can be found in lines 5 – 10 of page 8 of Applicant's disclosure.

Applicants respectfully request reconsideration of Examiner's rejection of claims 1 - 3 and 39 under 35 U.S.C. §103(a). Examiner has rejected these claims in view of the cited prior art reference of *Kubota et al.* (U.S. Patent No. 5,808,595). The *Kubota* reference, however, fails to teach or suggest the application of different threshold adjustment voltages to the second gate electrode during different time periods. For example, as taught at the top of pg. 23 of Applicant's disclosure, a threshold adjustment voltage of -5 V is applied when the transistor is in an off-state, and a threshold adjustment voltage of 0 V is applied when the transistor is in the on-state. As a second example, see the top of page 25 of Applicant's disclosure, which teaches the application of a threshold adjustment voltage of 10 V during selection time and a threshold adjustment voltage of -5 V during non-selection time. *Kubota* fails to teach or suggest such a device. Accordingly, Applicants respectfully request Examiner withdraw the 35 U.S.C. §103 rejection, and place all remaining claims in condition for allowance.

Examiner's remaining references cited but not relied upon, considered either alone or in combination, also fail to teach applicant's currently claimed invention. In light of the

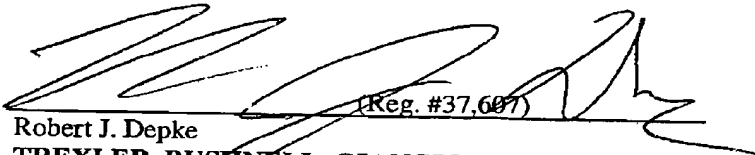
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foregoing, Applicants respectfully submit that all claims now stand in condition for allowance.

Date:

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